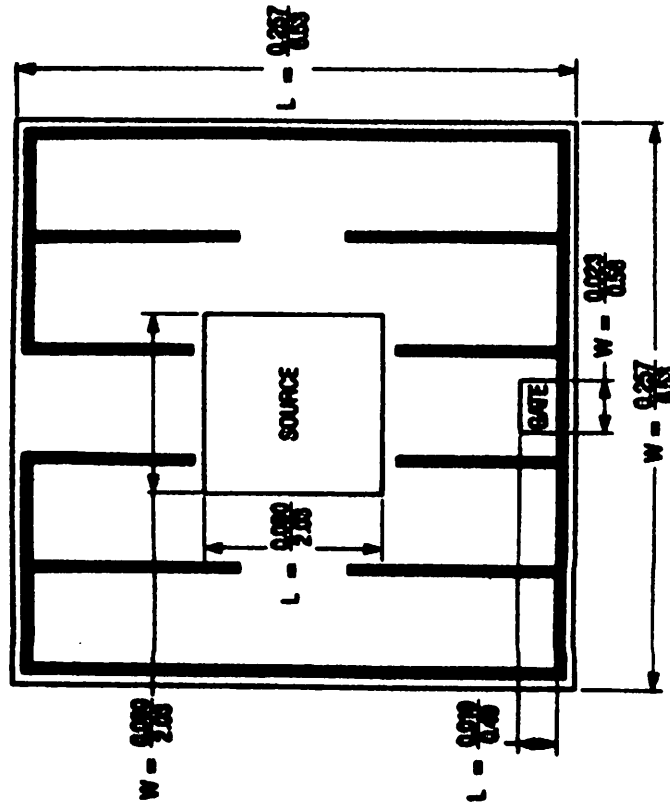




Sierra Components, Inc.

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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



D22

HEX-5: 60V, N-CHANNEL

Topside Metal: Al
Backside:
Backside Potential:
Mask Ref:
Bond Pads : .004"

APPROVED BY: CB
MFG: IRI

DIE SIZE : .257" x .257"
THICKNESS: .019"

DATE: 2/7/01
P/N: IRFC054

DG 10.1.2
 Rev A 3-4-99